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| APPLICANTS | | | | | | | | | |
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| Teruhito Ohnis Akira Asai, Ue | / | M.A. | | 0.1<. | | | | | |
| ** CONTINUING DATA This applicatio | | CON of PCT/JP02/09569 | 9 09/18/2002 | VAN | ٨ | o.K . | | | |
| ** FOREIGN APPLICA JAPAN 2001-2 | | | | V A.M | ٨ | o.K. | | | |
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| ADDRESS McDERMOTT, WILL 8 600 13th Street, N.W. Washington , DC 20005-3096 | EME | RY . | | | | | | | |
| TITLE Semiconductor device | and fa | abrication method thereo | f | | | | | | |
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